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Simon Min Sze (Chinese: ; pinyin: Shì Mìn; born 1936) is a Chinese-American electrical engineer.He is best known for inventing the floating-gate MOSFET with Dawon Kahng in 1967. Biography, Sze was born in Nanjing, China, and grew up in Taiwan.After graduating from the National Taiwan University in 1957, he received a master's degree from the University of Washington in 1960 and a ...

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A method for etching a layer of a microelectronic structure includes the steps of masking the layer to be etched so that predetermined portions of the layer are exposed, and providing an etching gas.

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Berkeley, M.I.T., University of Illinois, and many others In addition, the course has been taught for IBM, Xerox, Bel Labs, Hewlett-Packard, DEC, Varian Associates, Boeing Aerospace, and others. VLSI Technology, Inc., was formed in 1979 to support the design and implementation of VLSI circuits.

AI Lab logo
In 1962, Atalla and Kahng fabricated a nanolayer-base metal-semiconductor junction (M-S junction) transistor that used gold (Au) thin films with a thickness of 10 nm. In 1987, Iranian engineer Bijan Davari led an IBM research team that demonstrated the first MOSFET with a 10 nm gate oxide thickness, using tungsten -gate technology.

History of nanotechnology - Wikipedia
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