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istic never is better than -1, so the system never has any gain to compensate for noise. Exercise 1.81 The circuit functions as a buffer with logic levels VIL = 1.5; VIH = 1.8; VOL = 1.2; VOH = 3.0. It can receive inputs from LVCMOS and LVTTTL gates be- ... Digital Design and Computer Architecture, Exercise Solutions. SOLUTIONS = + + ...

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