# Analysis And Synthesis Of Fault Tolerant Control Systems

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In HiP-HOPS, this is achieved mechanically with the aid of a systematic algorithm for the synthesis of fault trees. Fault trees are constructed by exploiting the structure of the hierarchical model and information about the local failure behaviour of components that is contained in low-level IF-FMEAs.

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Fault sensitivity analysis (FSA) is a side-channel attack method that injects faults to cryptographic circuits through clock glitch- ing and applies statistical analysis to deduce sensitive data such as the cryptographic key. It exploits the correlation between the circuit's sig- nal path delays and sensitive data.

**Synthesis of Fault-Attack Countermeasures for ...**
In traditional fault diagnosis methods, diagnosticians analyze the collected signals with signal processing techniques, such as envelope spectrum analysis and wavelet analysis, to extract characteristics that reflect the fault information. The main characteristics used refer to the fault characteristic frequency and its harmonics.

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A procedure for automatically generating fault trees is presented. The fault trees describe nearly all the failure modes for the system under analysis. The fault tree generation procedure uses information on (1) the description of the system (detailed flowsheet), (2) physical and chemical properties of materials in and around the system, and (3) unit models which describe the behavior of the units within the system and which are assembled to describe the behavior of the complete system.

**Fault tree synthesis for chemical processes - Powers ...**
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